

ABSTRACT OF THE DISCLOSURE

A voltage amplification circuit is provided which is capable of faithfully amplifying an input signal even in an
5 inverting amplifier placed in a second stage being DC-coupled to
an inverting amplifier placed in a first stage. By DC-coupling
the inverting amplifiers and by setting an amplifying operation
starting input voltage in the inverting amplifier placed in the
first stage to be lower than that in the inverting amplifier placed
10 in the second stage, the voltage amplification circuit that can
provide a large amplification factor is realized. By configuring
so that a circuit to set a clamping voltage has substantially the
same configurations as a circuit to set an amplifying operation
starting input voltage in the inverting amplifier placed in the
15 second stage, the voltage amplification circuit is obtained that
can follow variations in parameters on manufacturing and provide
a wide operating margin and operate in a stable manner even though
variations in parameters on manufacturing or variations in a
source voltage occur.